

GENERAL DESCRIPTION

The EC4304C is a continuous mode inductive step-down and buck-boost converter, designed for driving single or multiple series connected LEDs from a voltage source higher than or lower than the LED voltage. It operates from input supply between 7V and 40V and provides and externally adjustable output current up to 1.5A with proper supply voltage and external components, the EC4304C can provide more than 50 watts of output power. This EC4304C includes the output switch and high-side output current sensing circuit, which uses an external resistor to set the nominal average output current.

The ADJ pin will accept either a wide range pulse dimming waveform or a DC voltage. This will provide either a continuous or gated output current depending upon the control frequency. The soft-start time can be increased by connecting an external capacitor from the ADJ pin to ground. The PWM filter components also serve as softstart time setting. Applying a 0.2V or lower to the ADJ pin turns the output off and switches the device into a low current standby state.

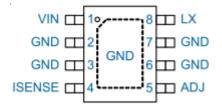
FEATURES

- Operates from 7V to 40V Supply Voltage
- Single Pin On/Off and Brightness Control Using DC Voltage or PWM
- Up to 95% Efficiency
- Internal 40V NDMOS Switch
- Up to 1.5A Output Current
- Typical +/-5% LED Current Accuracy
- Input Under Voltage Lockout
- SOP 8L(Exposed PAD) Package
- RoHS Compliant and Halogen-Free

Applications

- Automotive LED Lighting
- High Power LED Lighting
- Indicator and Emergency Lighting
- Architectural Lighting
- Low Voltage Industrial Lighting
- Signage and Decorative LED Lighting

Pin Configuration

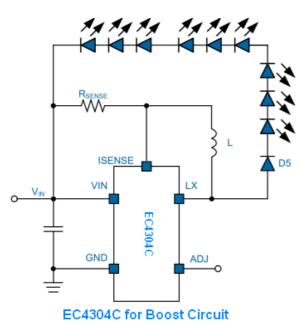


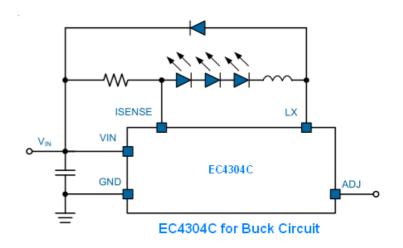
Pin Description

Pin No	Pin Name	Pin Function			
1	VIN	Power supply input pin. Connect a nominal $(7V~40V)$ power supply to this pin. The power- on-reset (POR) function monitors the input voltage by this pin. It is recommended that a decoupling capacitor (4.7µF or Higher X7R Ceramic capacitor) be connected to the GND for noise decoupling.			
2/3/6/7	GND	Ground for the IC. All voltages levels are measured with respect to this pin.			
4	ISENSE	Current Sense Pin: Sense LED String Current.			
5	5 ADJ ADJ -PWM signal input for digital PWM dimming.				
8	LX	Drain of NDMOS Switch.			
Exposed PAD		Ground and thermal Pad			



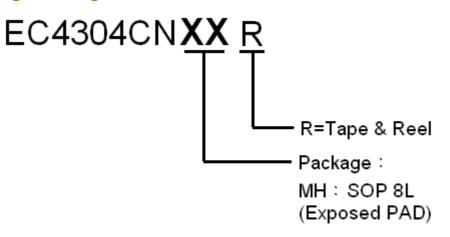
Typical Application





EC4304C

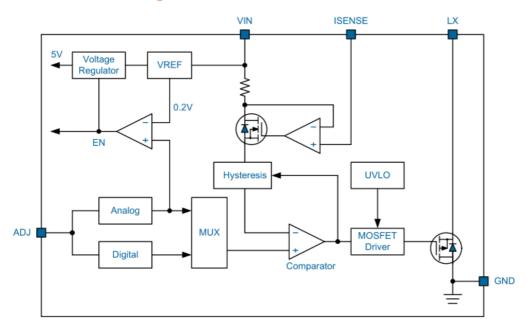
Ordering/ Marking Information



Part Number	Package	Marking	Marking Information
EC4304CNMHR	SOP 8L (Exposed PAD)	4304C LLLLL	LLLLL is Lot No DDDDDD : Date Code
	(LAPOSed FAD)	DDDDDD	



Functional Block Diagram



Functional Description

The EC4304C is a simple high-efficiency, continuous mode inductive step-down converter. The device operates with an input voltage range from 7V to 40V and delivers up to 1.5A of output current. A high-side current-sense resistor sets the output current and a dedicated PWM dimming input enables pulsed LED dimming over a wide range of brightness levels. A high side current-sensing scheme and an on-board current setting circuitry minimize the number of external components which is required while delivering LED current with +5% accuracy, using a 1% sense resistor.

Adjusting Output Current

The device contains a low pass filter between the ADJ pin and the threshold comparator and an internal current limiting resistor between ADJ and the internal reference voltage. This allows the ADJ pin to be overdriven with either DC or pulse signals to change the V_{SENSE} switching threshold and adjust the output current. Details of the different modes of adjusting output current are given in the applications section by:

$$I_{OUTdc} = \frac{V_{ADJ}}{1.25} \times \frac{100 \text{mV}}{\text{R}_{\text{SENSE}}} \quad \text{(for } 0.3\text{V} < \text{V}_{\text{ADJ}} < 2.5\text{V}\text{)}$$

The value of the output current is 1A at 0.1Ω (0.5A at 0.2Ω) and this is a calculated output current when the ADJ terminal is 1.25V floating.

Shutdown Mode

Taking the ADJ pin to a voltage below 0.2V for more than approximately 100µs will turn off the output, and supply current will fall to a low standby level of 20µA nominal.

Soft-Start

The device has inbuilt soft-start action due to the delay through the PWM filter. An external capacitor from the ADJ pin to ground will provide additional soft-start delay, by increasing the time taken for the voltage on this pin to rise to the turn-on threshold and by slowing down the rate of rise of the control voltage at the input of the comparator.

With no external capacitor, the time taken for the output to reach 90% of its final value is approximately 500µs. Adding capacitance increases this delay by approximately 0.5ms/ nF.

Inherent open-circuit LED protection

If the connection to the LED(s) is open-circuited, the coil is isolated from the LX pin of the chip, so the chip will not be damaged. Unlike in many boost converters, where the back EMF may damage the internal switch by forcing the drain above its breakdown voltage.





Absolute Maximum Rating(Note 1)

Supply Input Voltage, V _{IN}	-0.3V to + 45V
ISENSE Voltage, VISENSE	
LX Output Voltage, VLX	
Adjust Pin Input Voltage, VADJ	
Switch Output Current, ILx	
Operation Temperature Range	
Storage Temperature Range	55°C to +150°C
Junction Temperature	- 150°C
Lead Temperature (Soldering, 10 sec)	260°C
ESD Rating (Note 2)	
HBM (Human Body Mode)	4kV
MM (Machine Mode)	200V

Thermal Information

Package Thermal Resistance (Note 3)	
SOP 8L(Exposed PAD) θ _{JA}	47°C/W
SOP 8L(Exposed PAD) θ.c	17.9°C/W
Power Dissipation, $P_D @ T_A = 25^{\circ}C$	
SOP 8L(Exposed PAD)	2.13W

Recommended Operation Conditions

Operating Junction Temperature Range (Note 4)	40°C to +125°C
Operating Ambient Temperature Range	40°C to +85°C

Note 1. Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

- Note 2. Devices are ESD sensitive. Handling precaution recommended.
- Note 3. θ_{JA} is measured in the natural convection at $T_A = 25^{\circ}C$ on a high effective thermal conductivity test board of JEDEC 51-7 thermal measurement standard.
- Note 4. The device is not guaranteed to function outside its operating conditions.

Electrical Characteristics

 $(V_{IN} = 12V, T_A = 25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Supply Input	-					
Supply Voltage Range	Vin		7	-	40	V
VIN POR Threshold	Vinrth	V _{IN} Rising	-	4.95	-	V
VIN POR Threshold	VINFTH	V _{IN} Falling	-	4.8	-	V
Quiescent Current with Output Off	I_QOFF	ADJ pin grounded	-	20	40	μA
Quiescent Current with Output Switching	I_qon	ADJ pin floating f = 250KHz	-	0.5	1.0	mA
ISENSE Pin						
Current Sense Threshold Voltage	V _I sense	ISENSE pin with respect to VIN, ADJ = 1.25V	95	100	105	mV
ISENSE Input Current	I _{SENSE}	Vsense = V _{IN} -0,1	-	-	1.0	μA
Reference			-			
Reference Voltage	Vref	ADJ pin Voltage	-	1.25	-	V
Reference Voltage Temperature Coefficient			-	50	-	ppm/⁰K



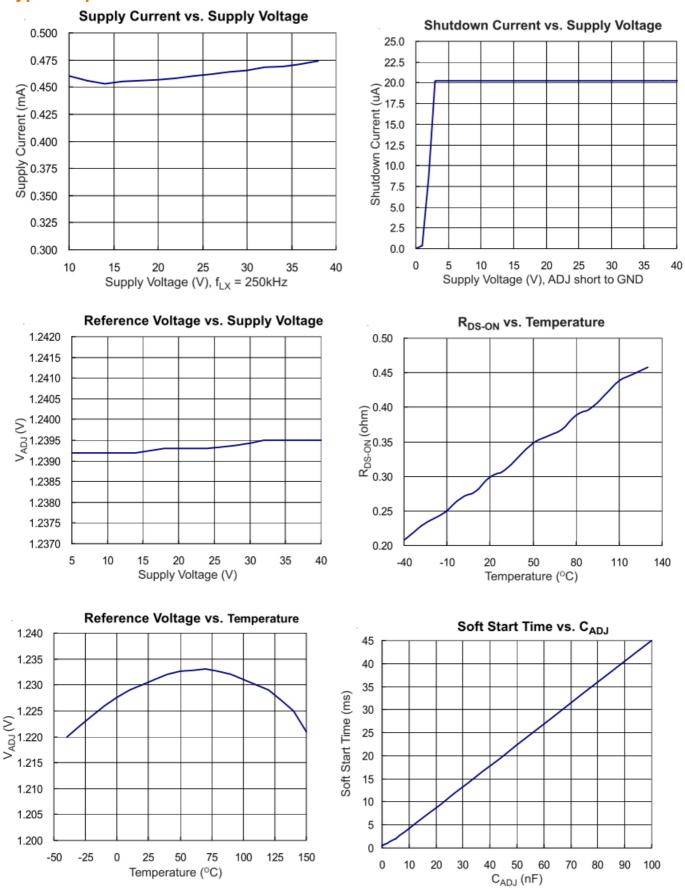
Electrical Characteristics

(V_{IN} = 12V, T_A = 25°C, unless otherwise specified)

(V _{IN} = 12V, T _A = 25℃, unless o Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
ADJ Pin					•	
ADJ Pin for DC Control Level	Vadj		0.3	-	2.5	V
ADJ to Switch Device from On State to Off State	Vadj_off	VADJ falling	0.15	0.2	0.25	V
ADJ to Switch Device from Off State to On State	Vadj_on	VADJ rising	0.2	0.25	0.3	V
	Radj	0 < VADJ < V _{REF}	135	-	250	KΩ
ADJ to VREF Resistance		$V_{ADJ} > V_{REF}$ +100mV	13.5	-	25	KΩ
ADJ Pin Low Frequency Duty Cycle Range	D _{PWM} (LF)	PWM frequency < 500Hz	10	-	100	%
ADJ Pin High Frequency Duty Cycle Range	D _{PWM} (HF)	PWM frequency >10KHz	16	-	100	%
LX pin						
LX Switch Current	LXmean		-	-	1.5	А
LX Rds_on	RLX	@I _{LX} = 1A	-	0.3	0.6	Ω
LX Leakage Current			-	-	5	μA
Minimum Switch ON Time	Ton_min	LX Switch ON	-	240	-	ns
Minimum Switch OFF Time	TOFF_min	LX Switch OFF	-	200	-	ns
Recommended Minimum Switch ON Time	TON_min_rec		-	800	-	ns
Frequency						
Operation Frequency	fLX	ADJ pin floating, L=33µH (0.093 Ω) Iout = 1A @ V _{LED} =3.6V driving 1LED	-	280	-	kHz
Recommended Maximum Operation Frequency	f LXmax		-	-	1	MHz
Duty Cycle Range of Output Switch of Operation Frequency	DLX		30	-	70	%
Internal Comparator Propagation Delay	t _{PD}		-	50	-	ns
Soft-Start						
Soft-Start Time	t _{ss}	Time taken for output current to reach 90% of final value after voltage on ADJ pin has risen above 0.3V.	-	500	-	μs

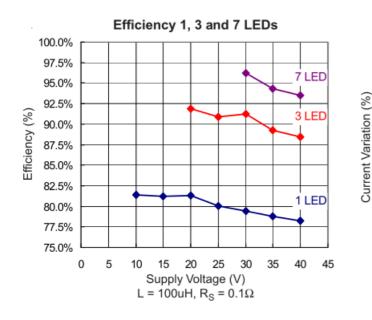


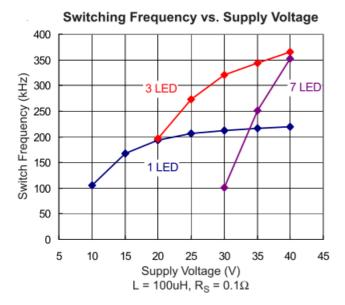
Typical Operation Characteristics

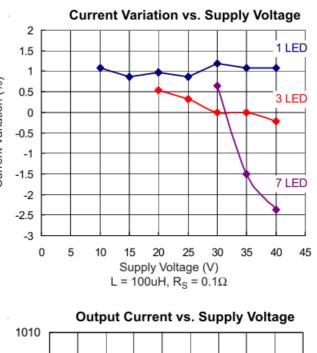


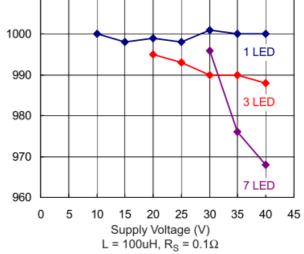


Typical Operation Characteristics







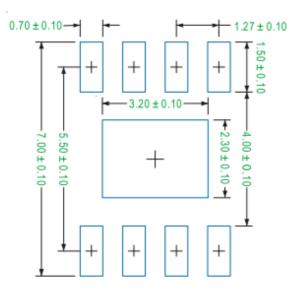


Output Current (mA)

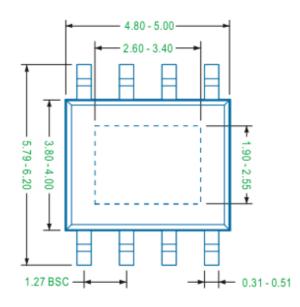


EC4304C

Package Information SOP 8L(Exposed PAD)



Recommended Solder Pad Layout





Note

1.Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

TYP. Typical. Provided as a general value. This value is not a device specification.

2.Dimensions in Millimeters.

3.Drawing not to scale.

4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.